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# USA SCT Barrel Module Assembly

SCT Week CERN

September 2003

Presented by Carl Haber

Lawrence Berkeley Lab

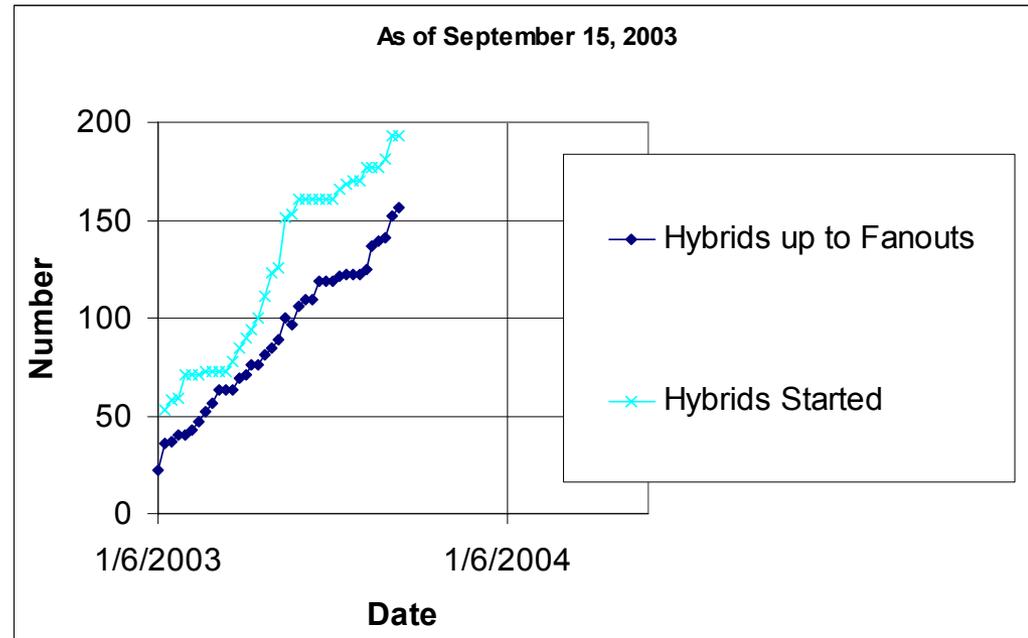
# Technical Status

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- Production started 9/02
- Qualification milestone passed 4/03
- Technical staff trained for production are in place, minimal cross training to spread expertise still underway
- 2 metrology systems in use
- 2 wirebonders in use (new H&K)
- Regularly building modules inside all specs
- Still experiencing occasional problems which require small adjustments to procedures
- Can just now align and glue 5 sides per day (2.5 modules) and complete 2 assemblies, ready for test, per day
- Full 483 deliverable should complete by 6/04

# Hybrid Production

- Strongly coupled to deliveries, so far just in time for module assembly
- 193 (152) started (completed)
- Delivery rate increased in last weeks, not yet reflected in assembly
- But concern persists about fanout bonding with ongoing tests and evaluations which are time consuming.
- Recent pitch adapter damage, origin under study



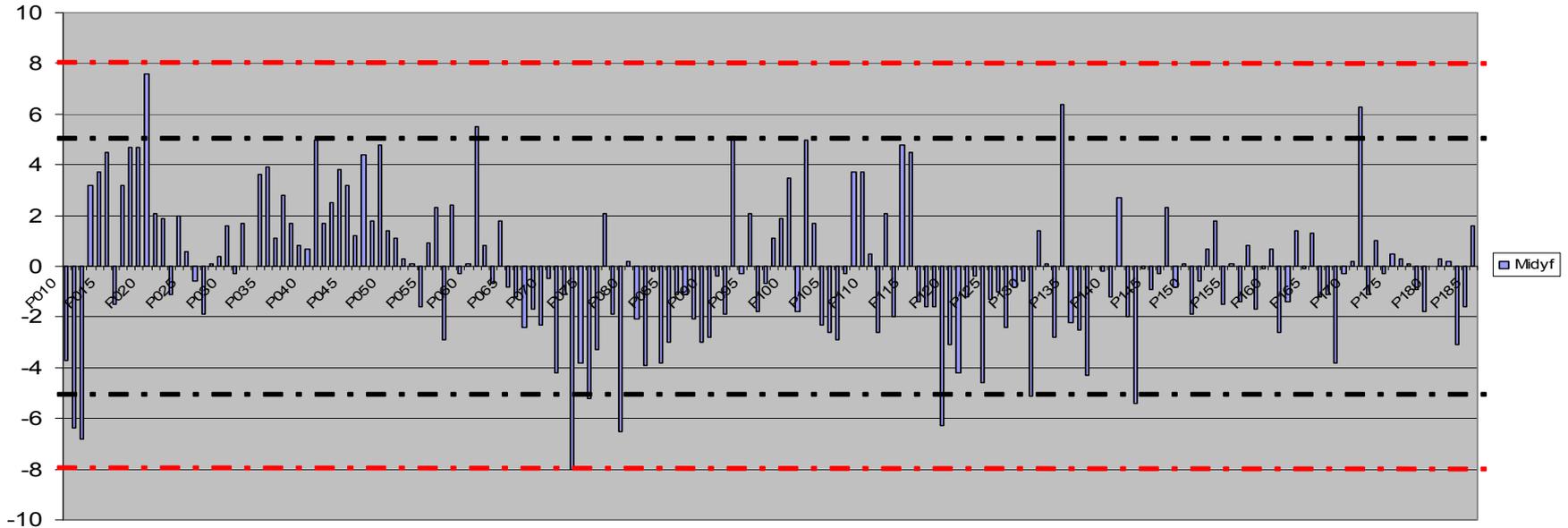
# Module Assembly and Test

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- What have been the key technical issues?
  - Metrology: meeting tightest specs on front to back alignment (“midyf”) and stereo angle, monitoring drifts
  - Wire-bonding: lost channels and re-bonding- a fanout issue
  - Leakage currents: with strict re-bonding protocols achieve ~“sum of the 4 crystals”
  - Leakage currents – significant breakdown above 350V
  - Hybrid attachment: height parameter, fixture systematics

# Metrology: Midyf history

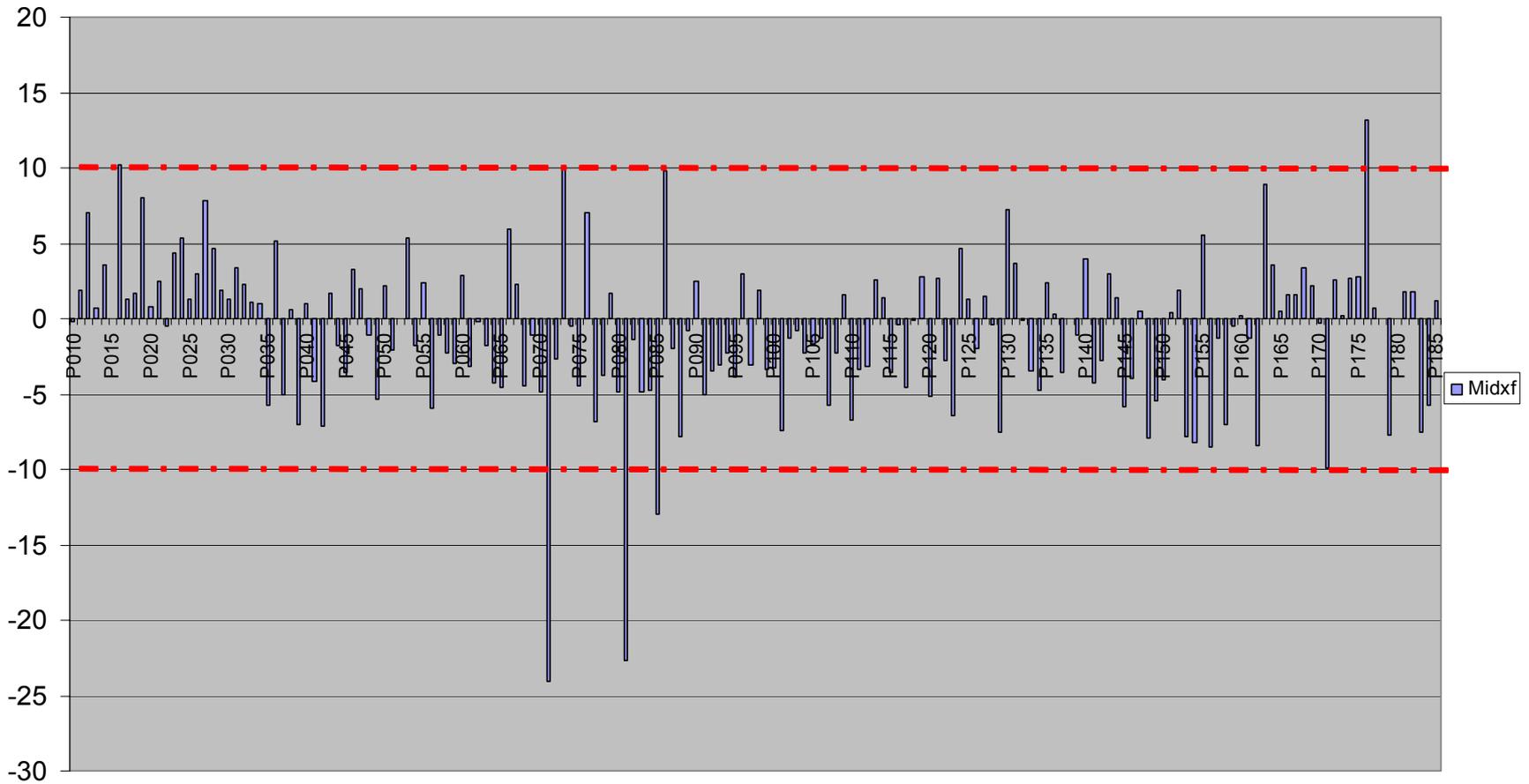
Midyf



Plot shows history since 11/02

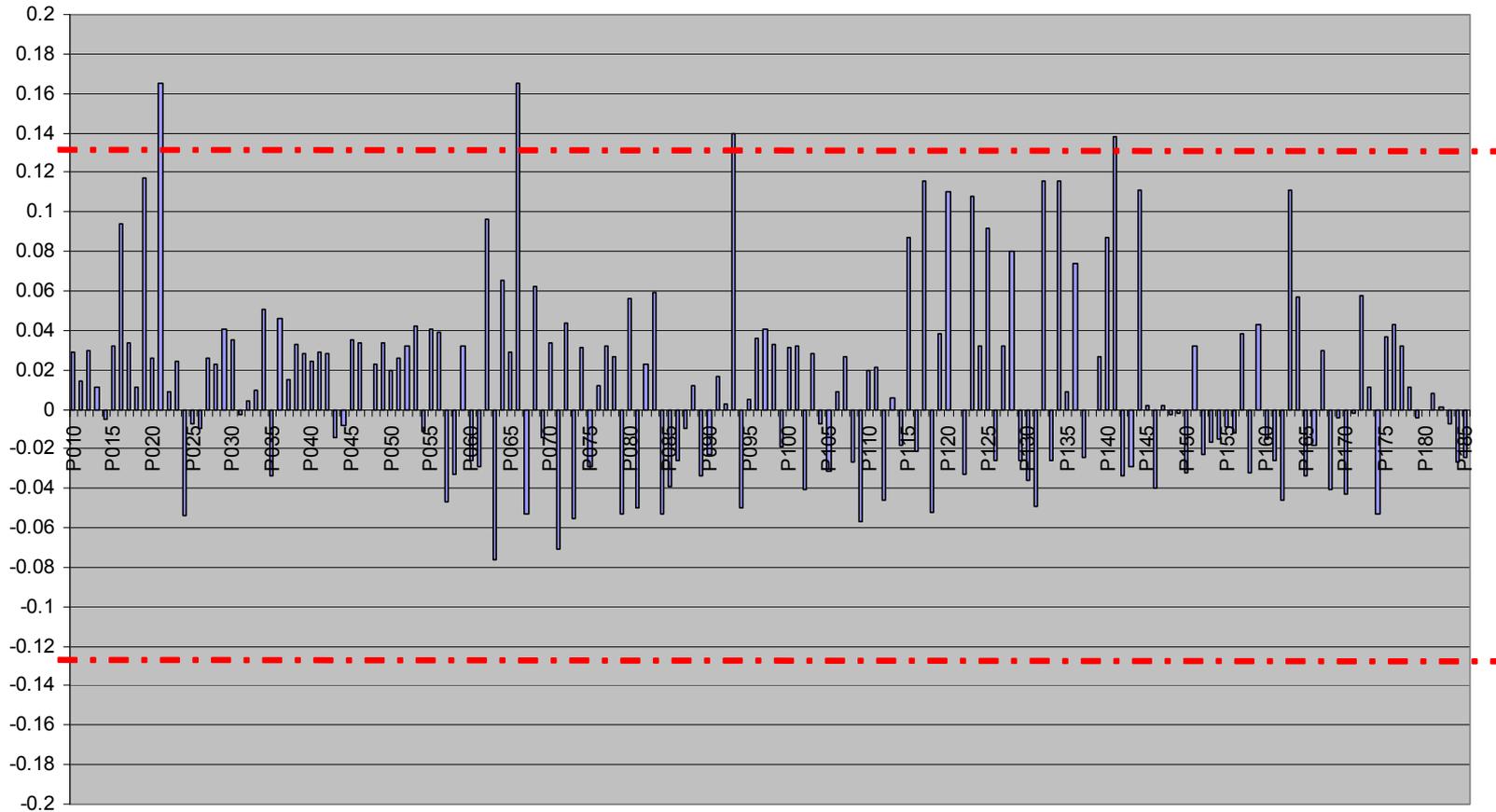
# Midxf (10 $\mu\text{m}$ )

Midxf



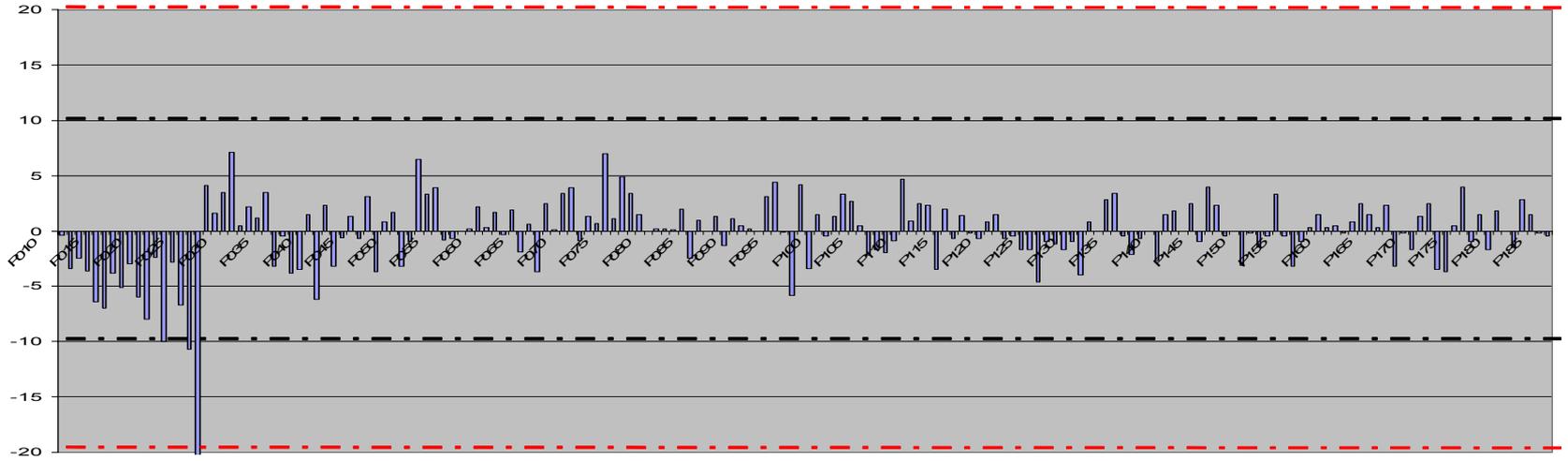
# Stereo Angle (130 mrad)

Stereo

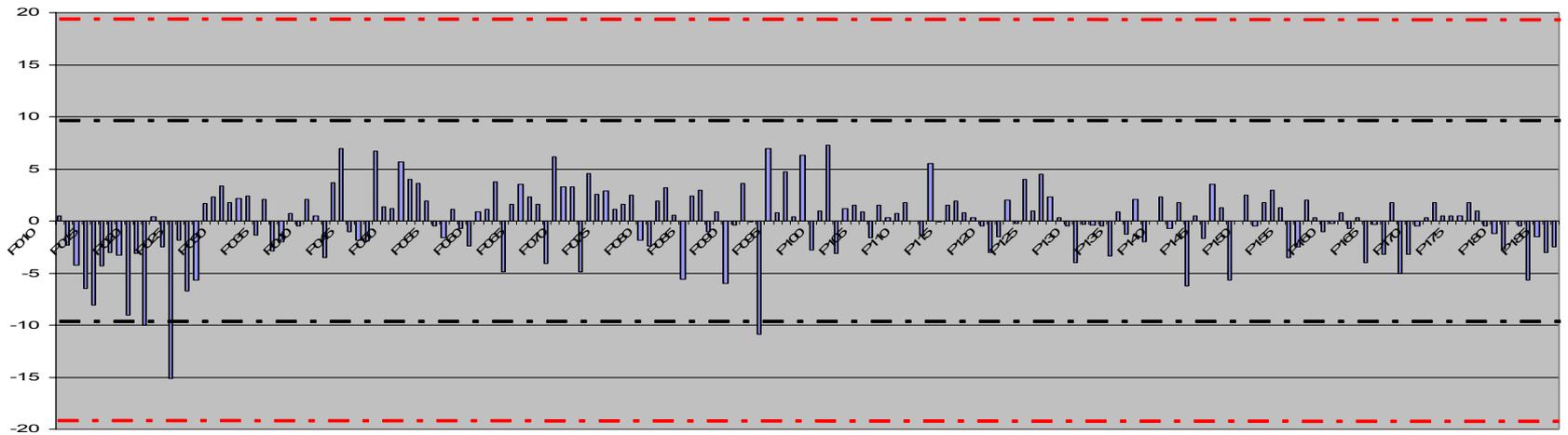


# Sepf and Sepb (10 $\mu\text{m}$ )

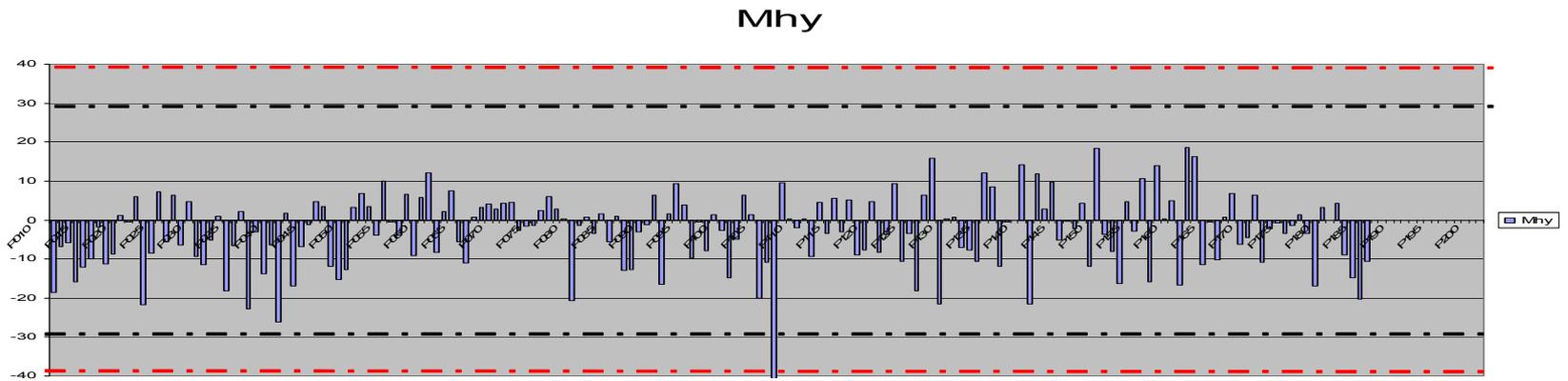
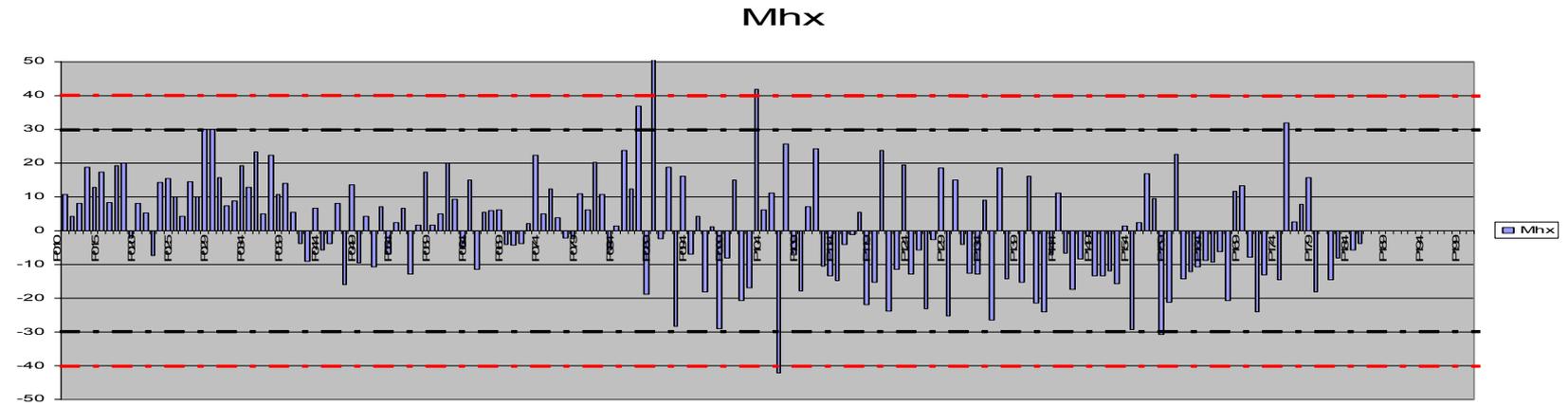
Sepf



Sepb

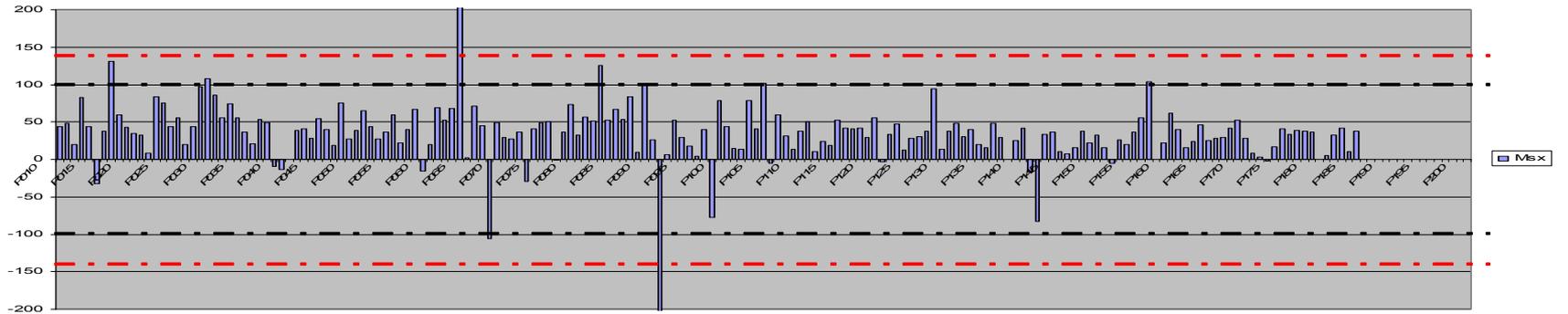


# Hole

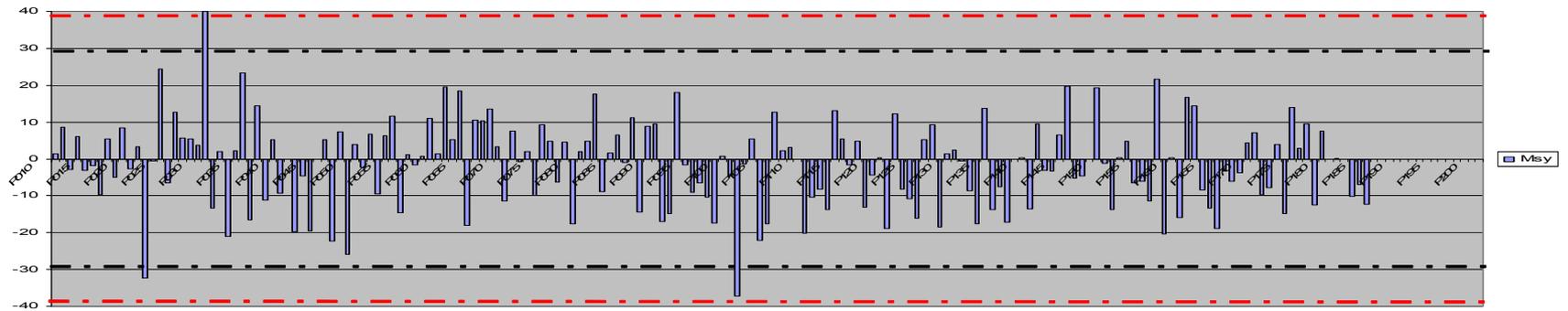


# Slot

Msx

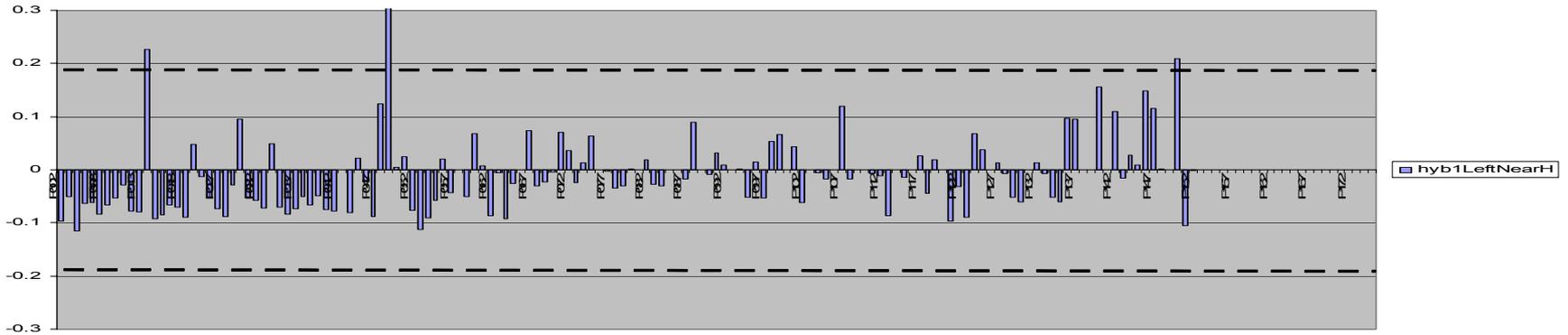


Msy

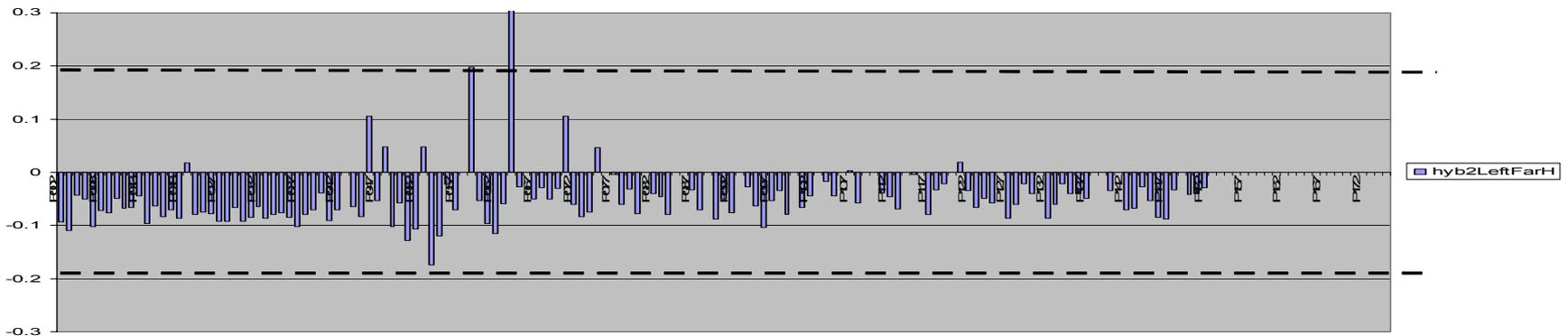


# Effects of Hybrid Mounting

hyb1LeftNearH

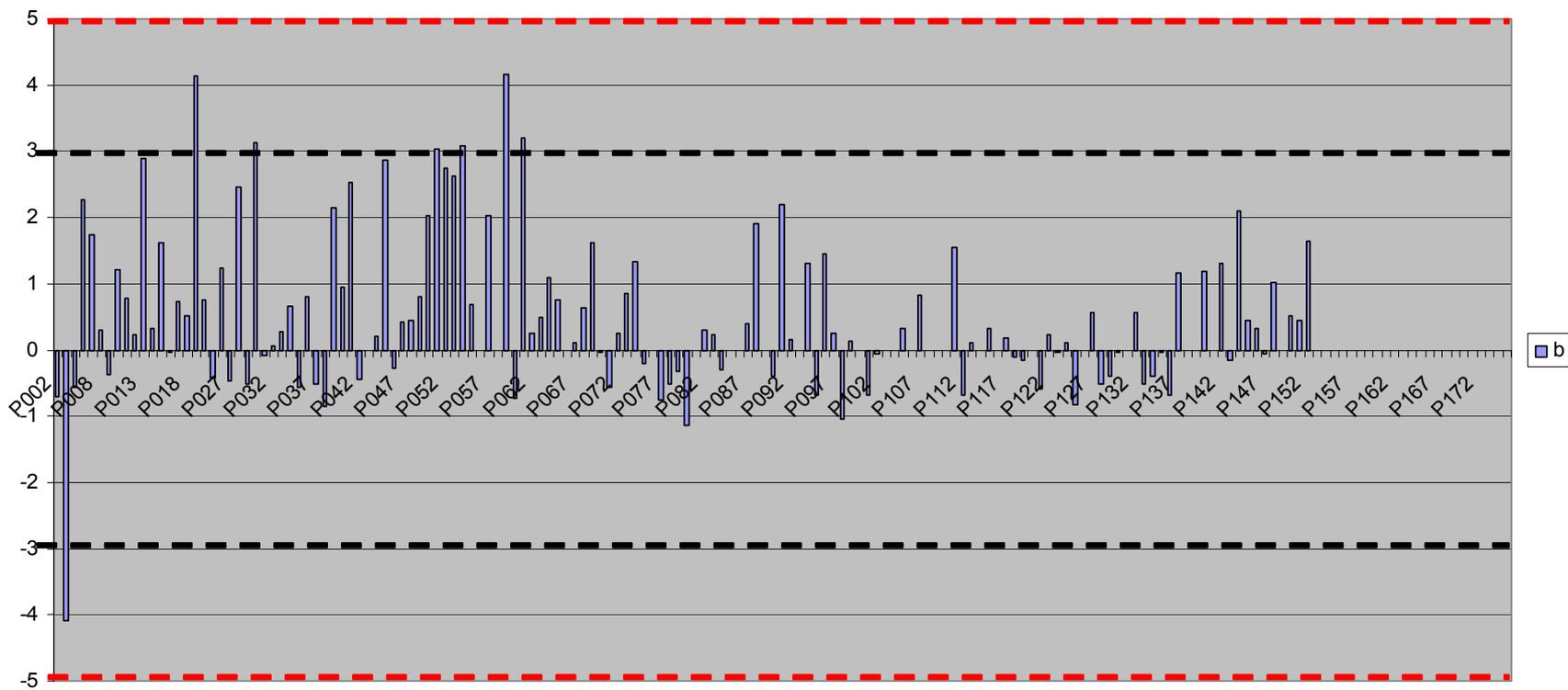


hyb2LeftFarH



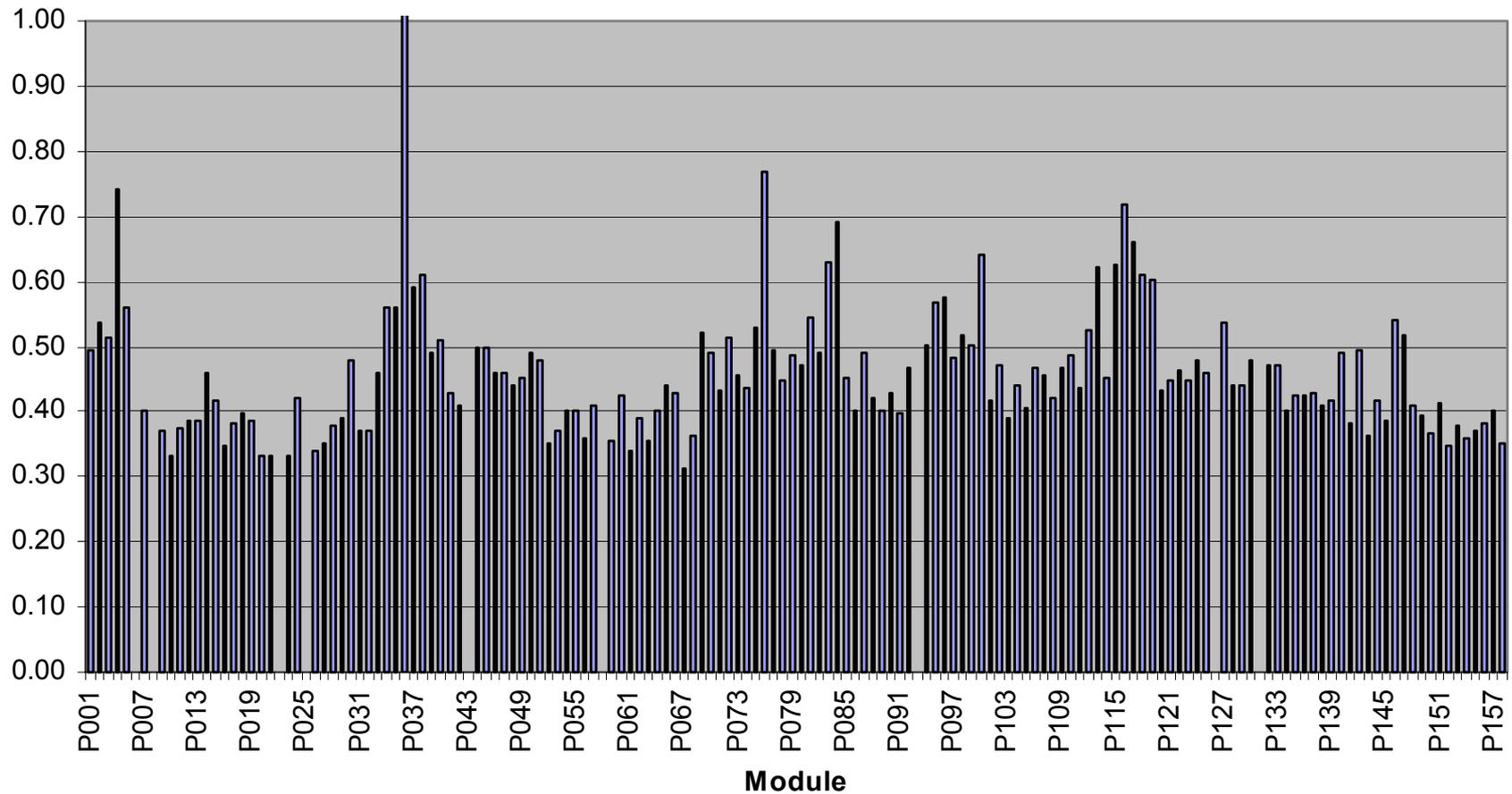
b

b



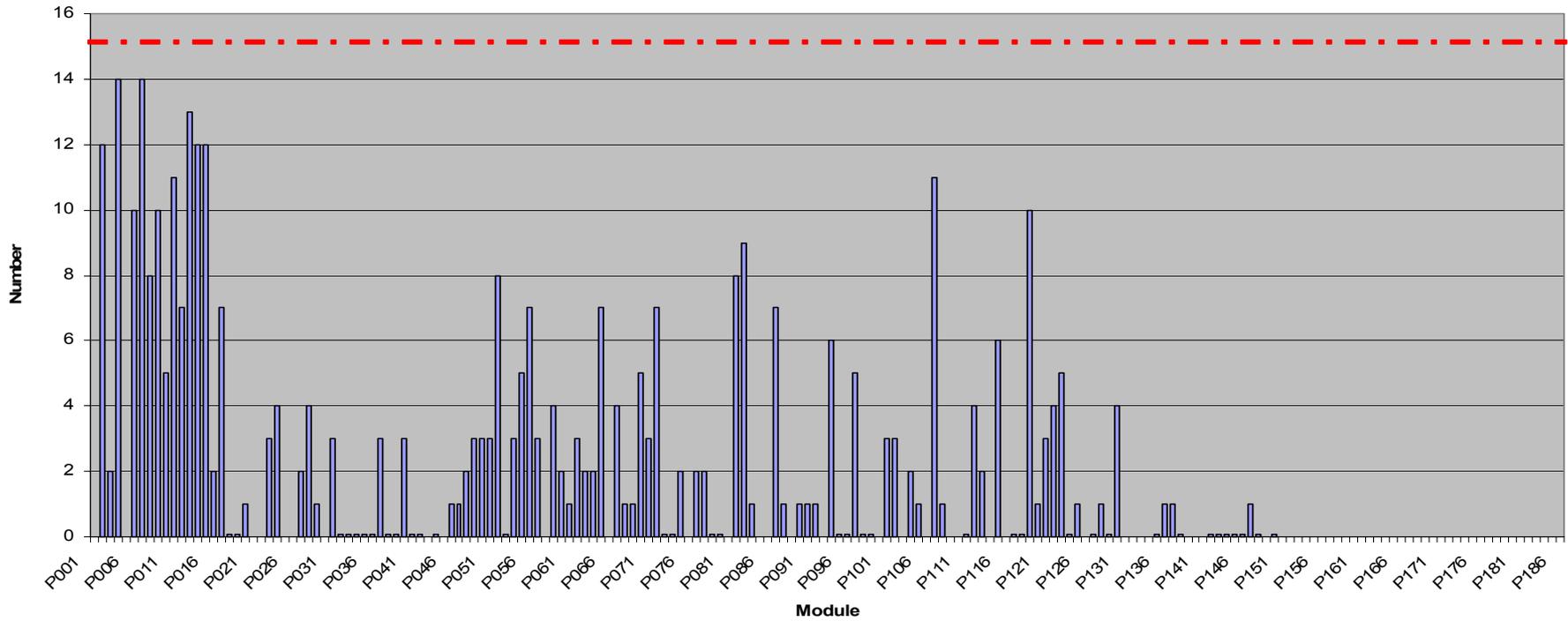
# Leakage Current of 4 Wafers

I of 4 detectors in microamps @ 500V at about 20C



# Bad Channels per Module

Total bad channels



# Leakage Current

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- About 7% of individual detectors show breakdown above 350V after 4 wafer assembly.
- Except for a few, these exhibit rapid training/conditioning in seconds to few minutes after 500V.
- About 15% of modules also show breakdown above 350V just after wire bonding.
- Again most of these, but not all, exhibit rapid training/conditioning to stable, low currents.
- Correlation between breakdown at 4-wafer stage and after wire bonding to hybrid is only about 50%.

# Module Categories

- We both classify modules as they are in the pipeline(as we go) and after tests are complete(or if they go to Hold, Fail, Rework). The yields are given for both all modules started and those completed tested/classified
- As of September 15.
- Modules started: 186. “Pipeline yield”
  - Modules Good finished or in pipeline:107(58%)
  - Modules Good+Pass finished or in pipeline:144(77%)
  - Modules Good+Pass+Hold finished or in pipeline:176(95%)
  - Modules Good+Pass+Hold+Rework finished or in pipeline:181(97%)
- Modules classified(tests done + Hold, Fail, Rework): 153. “Classified yield”
  - Modules Good: 78(51%)
  - Modules Good+Pass: 111(73%)
  - Modules Good+Pass+Hold: 143(93%)
  - Modules Good+Pass+Hold+Rework: 148(97%)
- Assembled modules with all tests done: 117

# Hold Categories

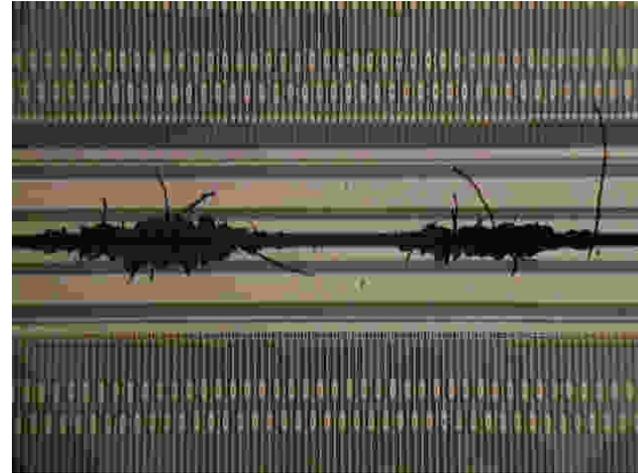
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- Metrology: 13 with parameters outside PASS
  - Module thickness – one case
  - Stereo: 0.168, 0.165, 0.165, 0.135, 0.138
  - Sepf -23
  - Midxf: -24, -23, -13
  - Mhx: 59, 42, -42
  - This problem is largely solved, hold rate much lower in last 50 modules
- Leakage current: 12
  - These need more study. Under real experiment conditions?
- Glue leakage: 7
  - 4 only in gap or minimal
  - 3 with glue on surface but not on pads
  - This problem has been solved. Last module with problem was P116 and we are now in P190s.

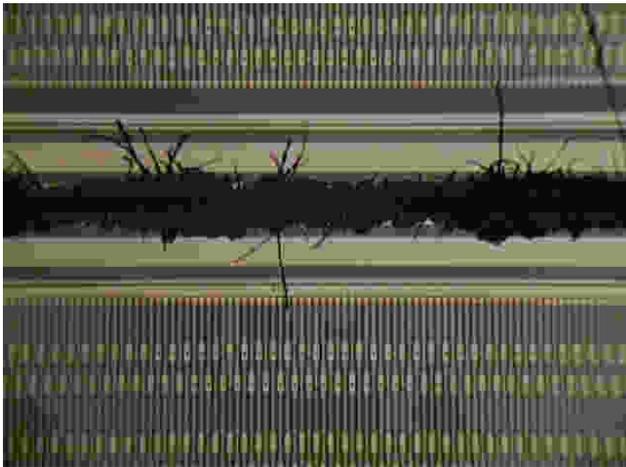
# Glue Leakage on Surface



Minimal glue on surface



6 spots like these



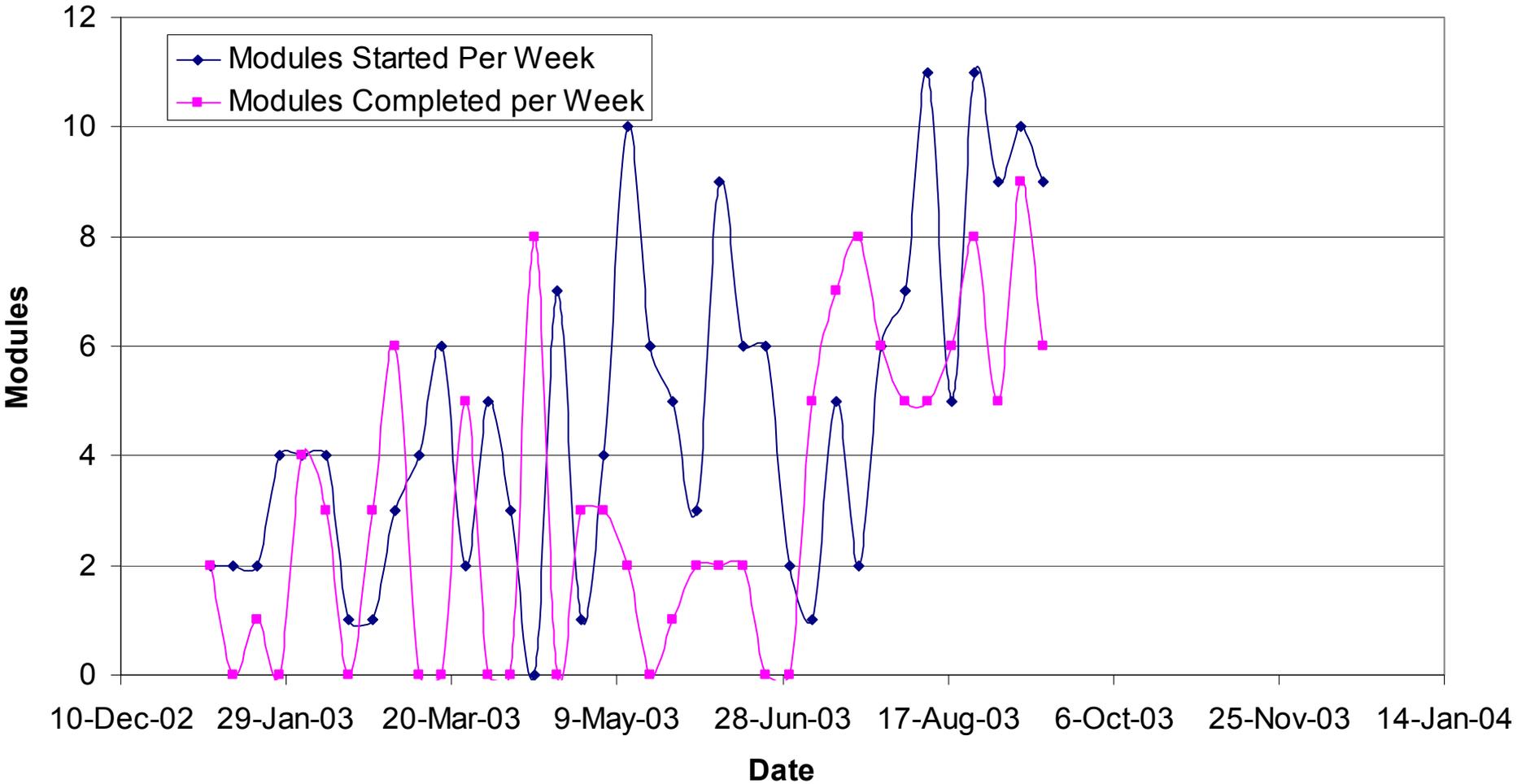
~50% of gap covered like this

# Fail and Rework Categories

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- Metrology: 2
  - Completely off due to wrong DIMS file
  - Vacuum failure during cure
- Broken: 3
  - Accidents
- Reworks: 5
  - 3 are hybrids too high
    - Have fixed one so far with hot wire to cut glue under foot
    - Expect to repair others eventually
  - 2 are bond damage
    - Expect to repair with some add'n bad channel or leakage

# Modules per week



# Production Model

- Based upon deliverable of 483 modules
- Based upon current rate, increasing to  $>2/\text{day}$  later in the project
- Include measured delay between assembly and test completion
- Project ends 6/04

